

AMENDMENTS TO THE CLAIMS

Please amend the claims as follows:

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1. (Currently Amended) A computer system comprising:

- a first video controller integrated into a chipset;
- a second video controller coupled to the chipset;
- a first display device;
- a second display device;
- a switching device configured to receive a first signal from the first video controller and a second signal from the second video controller, and configured to provide the first signal or the second signal to a first display device different signals from the video controllers at respective inputs and to provide each of the signals to a compatible one of the display devices;
- a chipset that includes the first video controller and the switching device, wherein the second video controller is coupled to the chipset;
- the switching device being coupled to a first connector configured to receive the first display device; and
- the switching device being coupled to a second connector configured to receive the second display device.

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2. (Cancelled).

3. (Cancelled).

4. (Previously Amended) The computer system of claim 1, wherein the chipset includes an AGP port, and wherein the second video controller is configured to provide the second signal to the switching device using the AGP port.

5. (Cancelled).

6. (Previously Amended) The computer system of claim 1, further comprising:
a processor coupled to the chipset; and
a system memory configured to store a program that is executable by the processor;
wherein the program includes instructions for causing the switching device to provide the first signal or the second signal to the first display device.

7. (Cancelled).

8. (Cancelled).

9. (Currently Amended) The computer system of claim 1, wherein the switching device is configured to provide the first signal or the second signal to the second display device signals include analog and digital signals.

10. (Currently Amended) A computer system comprising:
a first video controller integrated into a chipset;
an interface coupled to the chipset and configured to receive a second video controller;
a first and a second display device;
a switching device coupled to the first video controller and the interface;
wherein the switching device is configured to provide a first signal from the first video controller to a first display device in response to the second video controller not being coupled to the interface, and wherein the switching device is configured to provide a second signal from the second video controller to the first display device in response to the second video controller being coupled to the

interface receive different signals from the video controllers at respective inputs
and to provide each of the signals to a compatible one of the display devices;

~~a chipset including the first video controller and the switching device, the~~
~~interface being coupled to the chipset;~~

the switching device being coupled to a first connector configured to
receive the first display device; and

the switching device being coupled to a second connector configured to
receive ~~a~~ the second display device.

11. (Cancelled).

12. (Cancelled).

13. (Previously Amended) The computer system of claim 10, wherein the chipset
includes an AGP port, and wherein the AGP port is configured to receive the
second signal from the second video controller.

14. (Cancelled).

15. (Previously Amended) The computer system of claim 10, further comprising:
a processor coupled to the chipset; and
a system memory configured to store a program that is executable by the
processor;

wherein the program includes instructions for causing the switching device
to provide the first signal from the first video controller to the first display device
in response to the second video controller not being coupled to the interface, and
wherein the program includes instructions for causing the switching device to
provide the second signal from the second video controller to the first display

device in response to the second video controller being coupled to the interface.

16. (Cancelled).

17. (Cancelled).

18. (Currently Amended) The computer system of claim 10, wherein the switching device is configured to provide the first signal or the second signal to the second display device signals include analog and digital signals.

19. (Cancelled).

20. (Cancelled).

21. (Currently Amended) A method of providing a video signal to a display device in a scalable platform comprising:
 providing a first video controller integrated into a chipset;
 providing a first and a second display device;
 configuring an interface coupled to the chipset to receive a second video controller;
 coupling a switching device to the first video controller and the interface;
 configuring the switching device to provide a first signal from the first video controller to a first display device in response to the second video controller not being coupled to the interface;
 configuring the switching device to provide a second signal from the second video controller to the first display device in response to the second video controller being coupled to the interface;
 providing a chipset including the first video controller and the switching

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device, the interface being coupled to the chipset;
coupling the a switching device to a first connector configured to receive
the first display device; and
coupling the switching device to a second connector configured to receive
a the second display device,; and
whereby the switching device is configured to provide either of the first
and second signals to the second display device configuring the switching device
to receive different signals at respective inputs from the video controllers and to
provide each of the signals to a compatible one of the display devices.